



Sheet 1 of 1

Form 1449*	Atty. Docket No.: 303.229US2	Serial No. 09/132,157
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Leonard Forbes	
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## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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						Yes	No

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
MP	Wolf, S., "Silicon Processing for the VLSI Era", <u>Volume 2: Process Integration</u> , Lattice Press, Sunset Beach, CA, 338 & 355, Table 5.2, <del>(19990)</del> 1990

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\*Substitute Disclosure Statement Form (PTO-1449)

\*\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.